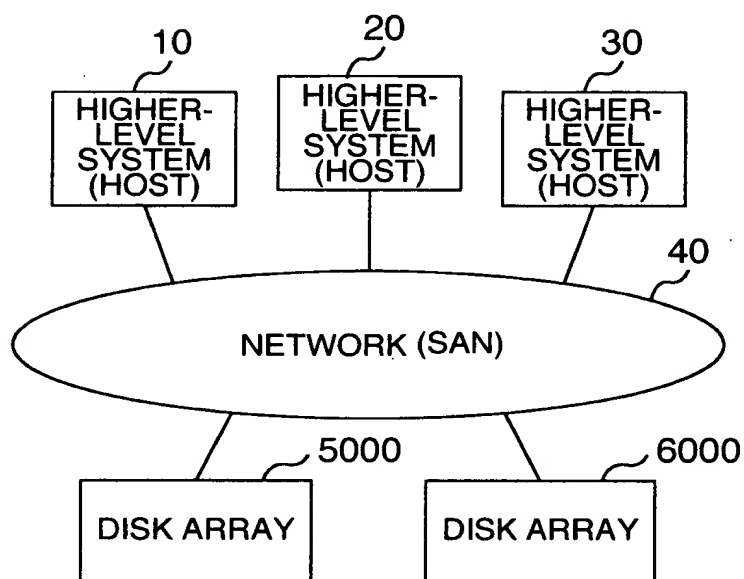


**FIG. 1**  
SYSTEM CONFIGURATION



**FIG. 3**  
DISK DEVICE GROUP

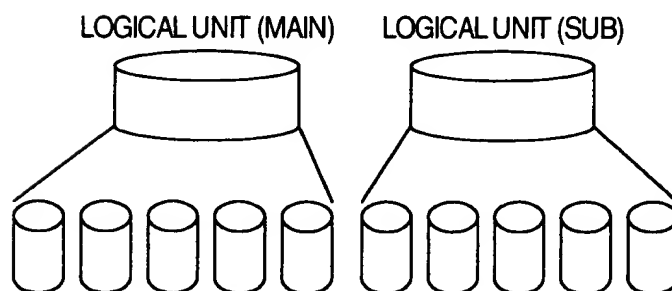
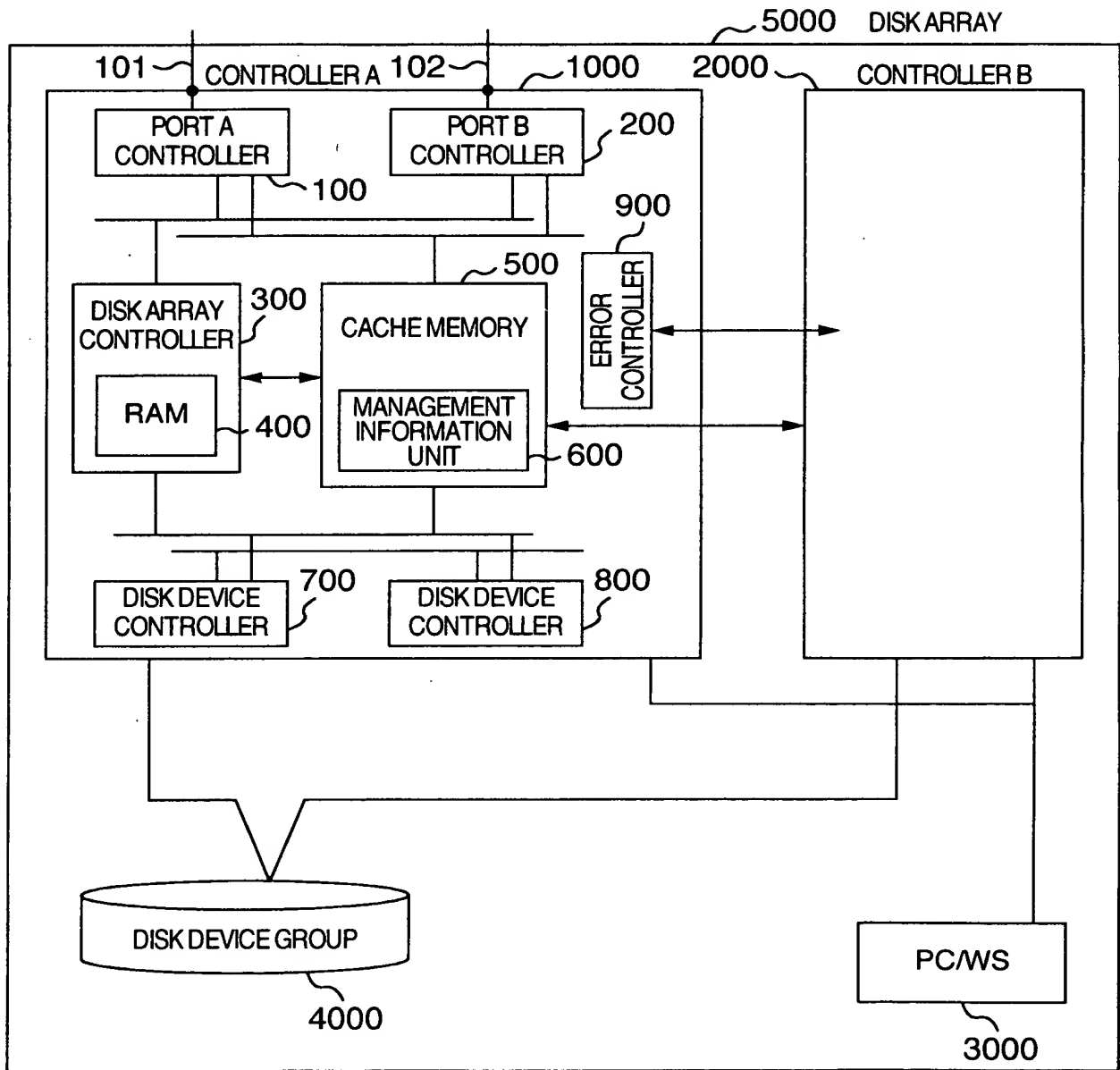


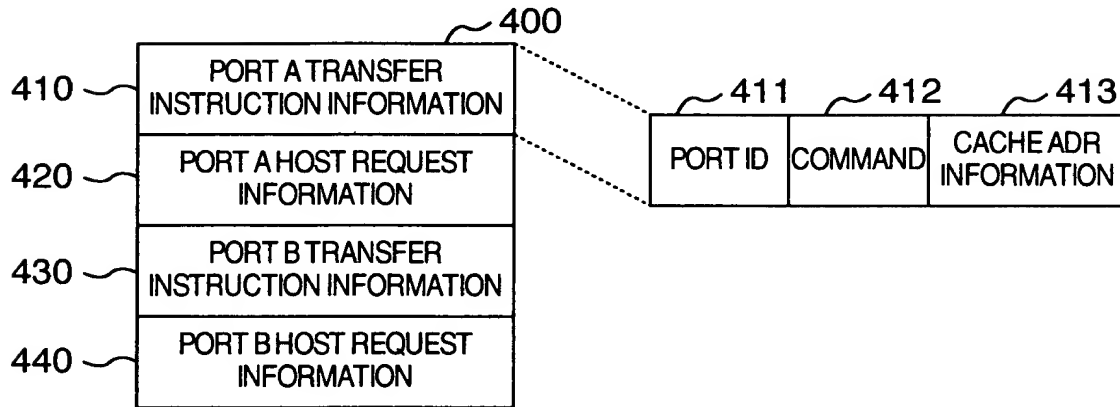
FIG. 2

DISK ARRAY CONTROLLER



# FIG. 4

INTERFACE INFORMATION BETWEEN PORT CONTROLLER  
AND DISK CONTROLLER (IN RAM)



# FIG. 5

PORT LOAD INFORMATION (IN RAM)

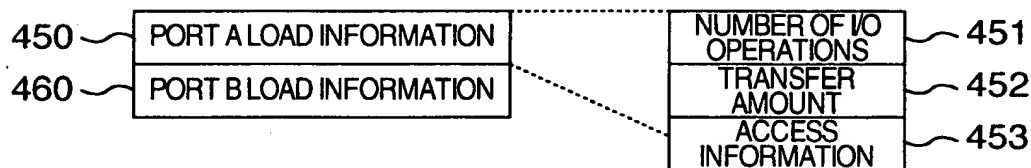
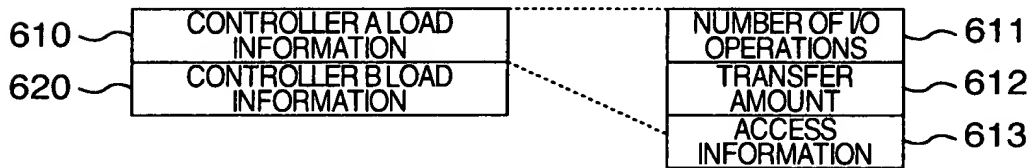


FIG. 6

# FIG. 6

CONTROLLER LOAD INFORMATION (IN CACHE)



# FIG. 7

BACKUP PROGRESS INFORMATION (IN CACHE)

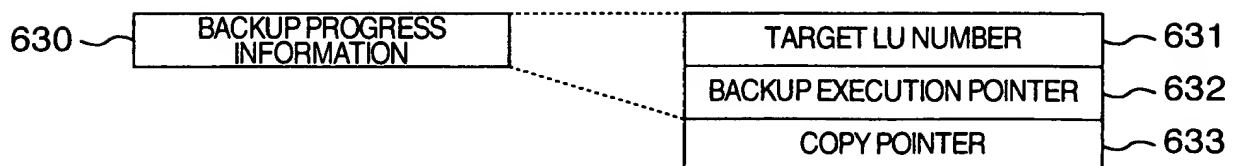


FIG. 8

USER SPECIFIED PORT INFORMATION (IN CACHE)

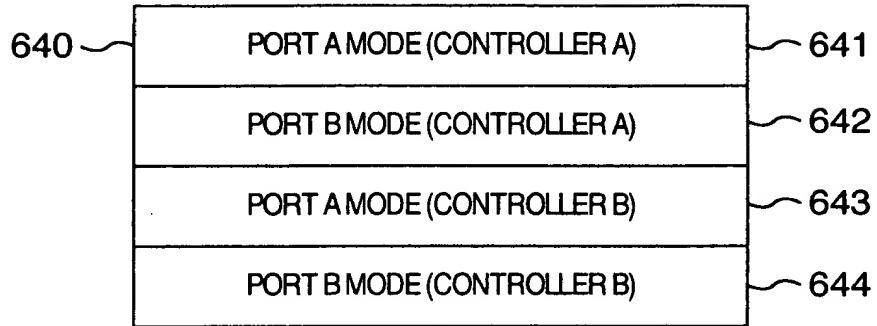


FIG. 9

COPY/BACKUP TIMING INSTRUCTION INFORMATION (IN CACHE)



FIG. 10

TIME SUPERVISION INSTRUCTION INFORMATION (IN CACHE)



FIG. 11

PRIORITY INFORMATION (IN CACHE)

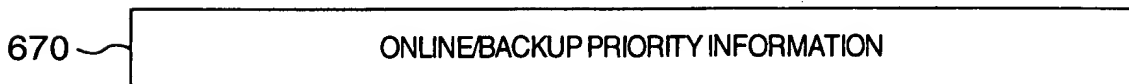


FIG. 12

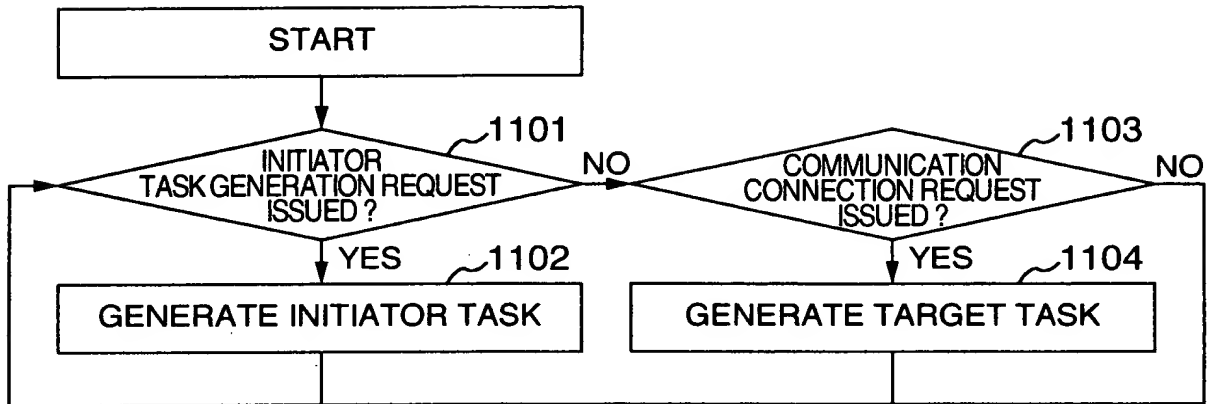


FIG. 13

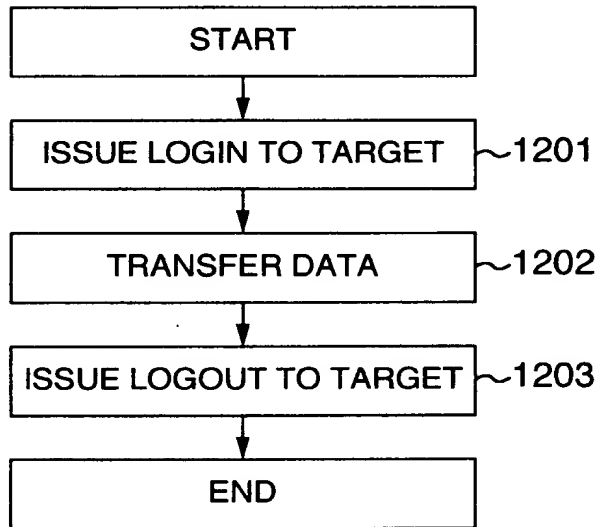


FIG. 14

